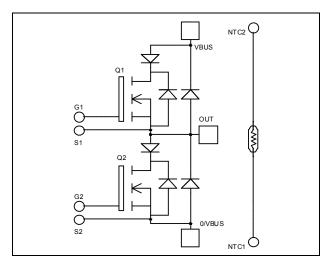


# Phase leg Series & parallel diodes MOSFET Power Module

$$\begin{split} V_{DSS} &= 1000V \\ R_{DSon} &= 230 m\Omega \ typ \ @ \ Tj = 25^{\circ}C \\ I_D &= 36A \ @ \ Tc = 25^{\circ}C \end{split}$$



O/VBUS

S2

#### **Application**

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### **Features**

- Power MOS 7<sup>®</sup> MOSFETs
  - Low R<sub>DSon</sub>
  - Low input and Miller capacitance
  - Low gate charge
  - Avalanche energy rated
  - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration



- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

## All ratings @ $T_j = 25$ °C unless otherwise specified

#### Absolute maximum ratings

VBUS

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		1000	V
Ţ	In (Continuous Drain Current	$T_c = 25$ °C	36	
1D		$T_c = 80$ °C	27	A
$I_{DM}$	Pulsed Drain current		144	
$V_{GS}$	Gate - Source Voltage		±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance		270	mΩ
$P_D$	Maximum Power Dissipation	$T_c = 25^{\circ}C$	694	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)		18	A
E <sub>AR</sub>	Repetitive Avalanche Energy		50	ma I
$E_{AS}$	Single Pulse Avalanche Energy		2500	mJ

OUT

OUT

NTC2

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

APTM100A23STG - Rev 5 October, 2013



### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 1000V$	$T_j = 25$ °C			200	A
		$V_{GS} = 0V, V_{DS} = 800V$	$T_j = 125$ °C			1000	μΑ
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 18A$			230	270	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 5 \text{mA}$		3		5	V
Ices	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}$ , $V_{DS} = 0 \text{ V}$	V			±150	nΑ

**Dynamic Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		8700		
$C_{oss}$	Output Capacitance	$V_{\rm DS} = 25V$		1430		pF
$C_{rss}$	Reverse Transfer Capacitance	f=1MHz		240		
$Q_{g}$	Total gate Charge	$V_{GS} = 10V$		308		
$Q_{gs}$	Gate – Source Charge	$V_{\text{Bus}} = 500V$		52		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 36A$		194		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C $V_{GS} = 15V$ $V_{Bus} = 667V$ $I_D = 36A$		10		
$T_{\rm r}$	Rise Time			12		ns
$T_{d(off)}$	Turn-off Delay Time			121		
$T_{\mathrm{f}}$	Fall Time	$R_G = 2.5\Omega$		35		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		1278		<b>T</b>
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 36A, R_G = 2.5\Omega$		760		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		2092		
$E_{\text{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 667V$ $I_D = 36A, R_G = 2.5\Omega$		902		μJ
$R_{thJC}$	Junction to Case Thermal Resistance				0.18	°C/W

## Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit	
$V_{RRM}$	Maximum Peak Repetitive Reverse Vol	tage		1000			V	
$I_{RM}$	Maximum Reverse Leakage Current	$V_R = 1000V$				500	μA	
$I_F$	DC Forward Current		$T_c = 65^{\circ}C$		90		A	
		$I_F = 90A$			1.9	2.3		
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 180A$			2.2		V	
		$I_F = 90A$	$T_j = 125$ °C		1.7			
+	Reverse Recovery Time $I = 0.0 A$	$T_j = 25$ °C		290		ng		
t <sub>rr</sub>		$T_{j} = 125^{\circ}C$		390		ns		
Q <sub>rr</sub>	Reverse Recovery Charge	Payarga Pagayary Charga	$di/dt = 400A/\mu s$	$T_j = 25$ °C		2010		nC
			$T_{j} = 125^{\circ}C$		7050		IIC	
$R_{thJC}$	Junction to Case Thermal Resistance				·	0.45	°C/W	



Parallel diode ratings and characteristics

Symbol	Characteristic Test Conditions		Min	Typ	Max	Unit	
$V_{RRM}$	Maximum Peak Repetitive Reverse Vol	tage		1000			V
$I_{RM}$	Maximum Reverse Leakage Current	V <sub>R</sub> =1000V				150	μA
$I_F$	DC Forward Current		$T_c = 80$ °C		80		A
		$I_F = 80A$			2.5	3.5	
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 140A$			3.1		V
		$I_F = 80A$	$T_j = 125$ °C		2		
+	Daviana Bassyany Tima		$T_j = 25$ °C		250		ne
$t_{rr}$	Reverse Recovery Time	$I_F = 80A$ $V_R = 667V$	$T_j = 125$ °C		315		ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt = 400A/\mu s$	$T_j = 25$ °C		830		C
		•	$T_{j} = 125^{\circ}C$		3300		nC
$R_{thJC}$	Junction to Case Thermal Resistance					0.65	°C/W

Thermal and package characteristics

Symbol	Characteristic			Min	Typ	Max	Unit
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz			4000			V
$T_{J}$	Operating junction temperature range			-40		150	
$T_{STG}$	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To Heatsink	M5	2.5		4.7	N.m
Wt	Package Weight					160	g

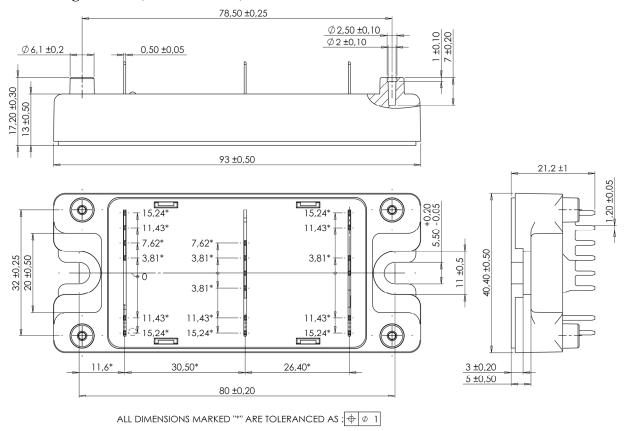
Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic		Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C	,		50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		T <sub>C</sub> =100°C		4		%

$$R_T = \frac{R_{25}}{\exp \left[ B_{25/85} \left( \frac{1}{T_{75}} - \frac{1}{T} \right) \right]}$$
T: Thermistor temperature R<sub>T</sub>: Thermistor value at T



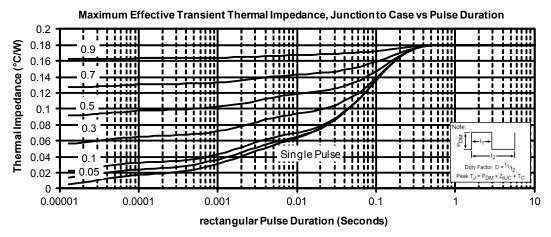
#### SP4 Package outline (dimensions in mm)

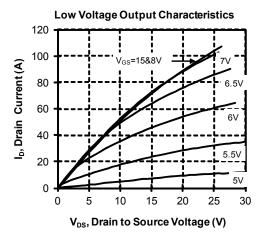


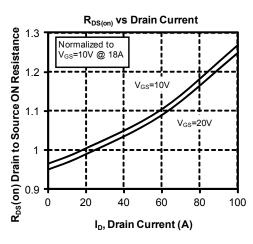
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

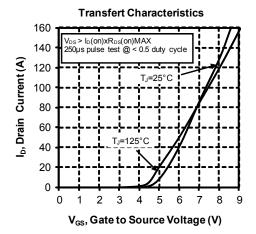


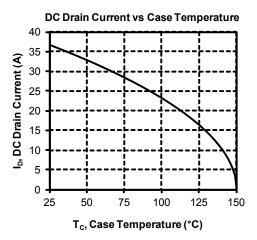
#### **Typical Performance Curve**



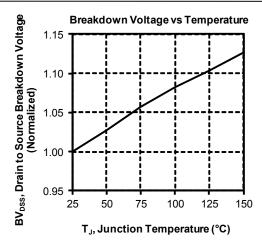


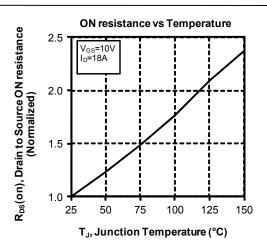


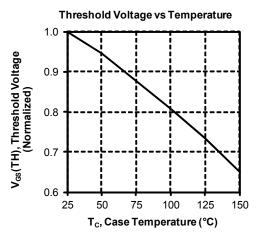


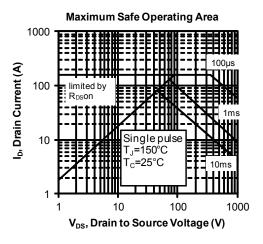


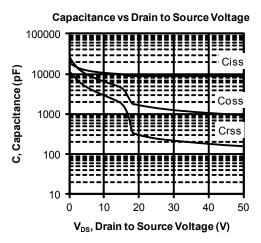


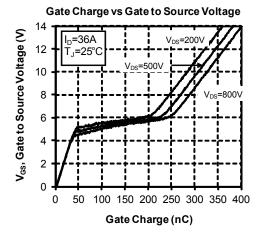




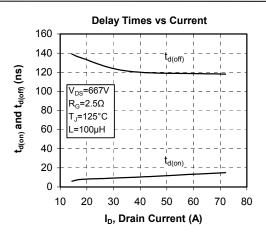


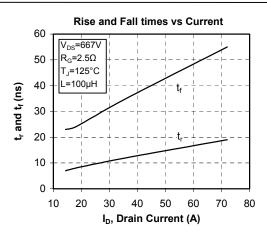


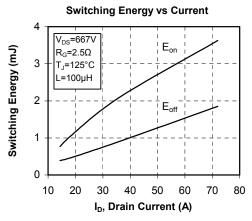


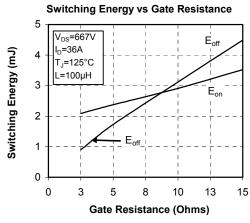


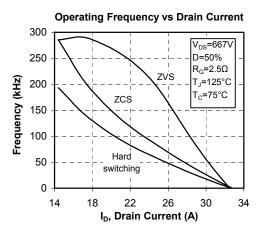


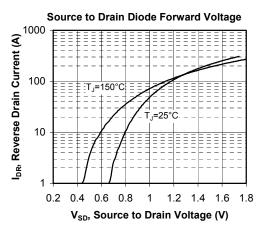














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